



DEFENCE INSTITUTE OF ADVANCED TECHNOLOGY

(Deemed to be University U/s 3 of UGC Act, 1956)

Department of Defence Research & Development

Ministry of Defence, Govt. of India

Girinagar, Pune - 411 025



ADVT. No. 21-2026/SRF/JRF/PA/DIAT

Date: 31 August 2026

RA/SRF/JRF/PA RECRUITMENT- CALL FOR APPLICATION

1. The Department of Electronics Engineering of this Institute is looking for young Scientists/Engineers for the positions of **SRF/JRF/PA** to work under **DRDO sponsored research project** entitled "**FxxN**". Details/eligibility conditions for selection are given hereunder: -

Sr. No.	Name of Post	No. of Post	Educational Qualifications required	Upper Age-Limit	Monthly Stipend Payable	Tenure
1.	Senior Research Fellow (SRF)	01	Essential: B.E./ B.Tech/ M.Sc. in Electronics/ E&C/ EEE/Aerospace with First Division with NET/GATE OR M.E./M.Tech in Radar System/ Communication/ Microwave Engg/ RF Systems & Antenna with First Division both at Graduate & Post Graduate level. <u>A minimum 02 years of research/ teaching/ industrial experience in the relevant fields is mandatory to the position of Senior Research Fellow (SRF)</u>	Not to exceed 30 Years. (*)	₹42,000/- (Consolidated) per month	The position is purely temporary for 06 months or Co-terminus with the project, whichever is earlier.
2.	Junior Research Fellow (JRF)		Essential: B.E./ B. Tech/ M.Sc. with First Division with NET/GATE. OR M.E./M.Tech in First Division both at Graduate & Post Graduate level.	Not to exceed 28 Years. (*)	₹37,000/- (Consolidated) per month	
3.	Project Associate (PA)		Essential: B.E./ B. Tech/ M.Sc. with First Division	Not to exceed 28 Years. (*)	₹ 25,000/- to ₹28,000/- per month, based on experience.	
Specialization:			Specialization: (For JRF/PA & SRF) Radar Signal Processing, Array Signal Processing/ AI/Image Processing/ Signal-processing/ Electronics/ Radar Signal Processing/ Array Signal Processing Desirable: - Strong hands-on design knowledge in (a) CST Studio/HFSS, (b) MATLAB with radar/ IP tools, (c) VHDL/IP-core/SDK for real-time DSP/RSP, (d) SDR based system design, (e) Micro controls / processor (f) Raspberry pi & (g) Python for AI design and image Processing - Familiar with RF instruments and advanced FPGA/SDR/ Microprocessor platforms.			

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Note:

- 1) The advertisement is valid for **three months** from the date of publication. During the validity of this advertisement, the interview/recruitment process are as follows:
 - a) Once in every 15 days, the applications will be shortlisted.
 - b) Shortlisted candidates will be informed and subsequently interview will be conducted.
 - c) Upon recruiting the project staff, if any, the vacancy will be reduced, accordingly, in the standing advertisement.
 - d) *The date of interview will be considered for determining the upper age limit. Upper age limit is relaxable up to 05 years for SC/ST, Women & PwBD and 03 years for OBC (NCL) candidates.
 - e) The tenure shall be fixed with the remaining duration of the project (06 months as on date).
 - f) The date for considering the educational qualifications and / or experience shall be the date of Interview.
 - g) **Recruited Fellows shall be encouraged to register for Ph.D. at this institute through proper channel as per DIAT norms.**
 - h) In addition to the monthly stipend, medical facilities limited to MI Room facilities shall be extended to the incumbent.
 - i) The mode of interview will be “in-person” or “via video conferencing” whichever is suitable to DIAT and shall be intimated to the shortlisted candidates.
 - j) Based on the technical requirements, the selected Fellow may be required to visit different establishments for the purpose of project.
 - k) Candidates having any national fellowship may also apply to work in this research project.

How to Apply: A Brief bio-data duly signed, Application form (available on the website), Proof of Date of Birth, BE./B.Tech M.E/M.Tech mark sheets and degree certificates may be sent to the email ID of the Principal Investigator (brazilraj.a@diat.ac.in) in PDF form as a single file, with a subject titled “**Application for SRF/JRF/PA**”.

2. Based on the suitability for the project, the applicants will be called for an interview (in person or via video conference). The shortlisted (for the interview) candidate will be individually informed via email. No TA/DA will be paid for attending the interview of any mode.
3. Candidates have to have their original applications with all necessary educational and other supporting documents/project reports/publications etc., at the time of the interview.
4. **Other Terms & Conditions: -**
 - a) Offer of RA/SRF/JRF/PA positions do not confer any right for absorption in DIAT (DU), Pune.
 - c) The qualification prescribed/ experience gained should have been obtained from recognized Universities / Institutions/Industries.
 - d) The prescribed educational qualifications are the bare minimum and mere possession of the same does not entitle the candidates to be considered at every stage of the selection process. In case of a large number of applications, based on the recommendations of the Screening Committee, the Principal Investigator may restrict the number of candidates to be called for the interview to a reasonable limit after taking, the qualifications and experience over and above the minimum prescribed in the advertisement, into consideration. Therefore, it is the interest of the candidates, to mention all the qualifications and experience in the application clearly with supporting documents.
 - e) **Selected candidate is required to join duty immediately on receipt of the offer letter.**
5. For further details/clarification, please contact **Dr. A A Bazil Raj, PI**, Dept. of Electronics Engineering on Telephone No. (020) 2460 4524 /81/ 31.
