



DEFENCE INSTITUTE OF ADVANCED TECHNOLOGY

(Deemed to be University U/s 3 of UGC Act, 1956)

Department of Defence Research & Development

Ministry of Defence, Govt. of India

Girinagar, Pune - 411 025



ADVT. No. 38-2024/PA/DIAT (DU)

PROJECT ASSISTANT RECRUITMENT- CALL FOR APPLICATION

1. The Department of Electronics Engg. of this Institute is looking for young Scientists/Engineers for the position of 'Project Assistant (PA)' to work under an Institute level research project entitled "*UWB radar design for RF characteristics investigation of intelligent stealth material*". Details/eligibility conditions for selection are given hereunder:

Sr. No.	Name of Post	No. of Post	Educational Qualifications required	Upper Age-Limit	Monthly Stipend Payable	Tenure
1.	Project Assistant (PA)	01	Essential: B.E./ B. Tech in Electronics Engg/ Electronics and Communication Engg/ Electronics and Telecommunication Engg or equivalent. Desirable skills: <ul style="list-style-type: none">• Basic knowledge of programming/ Design of MATLAB/ ADS (Advanced Design System)/ HFSS software required.• Good writing and communication skills in English.	Not to exceed 28 Years. (*)	₹20,000/- (Consolidated)	The position is temporary for 07 Months (OR) co-terminus with the project, whichever is earlier.

Note:

1. (*) 'The crucial date' for determining the upper age limit is **03 October 2024**. The upper age limit is relaxable up to 05 years for SC/ST, Women & PwBD and 03 years for OBC (NC) candidates.
2. Hostel accommodation may be provided subject to the availability on payment basis
3. In addition to the monthly stipend, medical facilities limited to MI Room facilities shall be extended to the incumbent.
4. Mode of the interview will be "in-person" or "via video conferencing" whichever is suitable to DIAT and shall be intimated to the shortlisted candidates.

2. **How to Apply:** A Brief bio-data duly signed, Application form (available on the website), Proof of Date of Birth, M.Sc./BE./B.Tech M.E/M. Tech mark sheets and degree certificates may be sent to the email ID of the Principal Investigator (kailassawant@diat.ac.in) in PDF form as a single file, with a subject titled "**Application for Project Assistant**" on or before **03 October 2024**.
3. Based on the suitability for the project, the applicants will be called for an interview (in person or via video conference). The shortlisted (for the interview) candidate will be individually informed via email. No TA/DA will be paid for attending the interview of any mode.
4. Candidates have to have their original applications with all necessary educational and other supporting documents/project reports/publications etc., at the time of interview.
5. **Other Terms & Conditions: -**
 - a) The offer of Project Assistant position does not confer any right for absorption in DIAT (DU), Pune.
 - b) The qualification prescribed/ experience gained should have been obtained from recognized Universities Institutions/Industries.
 - c) The prescribed educational qualifications are the bare minimum and mere possession of the same does not entitle the candidates to be considered at every stage of the selection process. In case of a large number of applications, based on the recommendations of the Screening Committee, the Principal Investigator may restrict the number of candidates to be called for the interview to a reasonable limit after taking, the qualifications and experience over and above the minimum prescribed in the advertisement, into consideration. Therefore, it is in the interest of the candidates, to mention all the qualifications and experience in the application clearly with supporting documents.
 - d) **Selected candidate is required to join duty immediately on receipt of the offer letter.**
6. For further details/clarification, please contact **Dr. K K Sawant, Principal Investigator**, Dept. of Electronics Engineering on Telephone No. (020) 2460 4522/ 4525 / Mobile No. +91 9850981196.
