

DIAT Join hands with NIELIT

Announced Joint M Tech program in VLSI & Embedded systems

Defence Institute of Advanced Technology (DIAT), Pune, under Department of Defence R&D, Min. of Defence, Govt. of India and National Institute of Electronics & Information Technology, Calicut under the Ministry of Electronics & Information Technology (MoEIT), Government of India signed an agreement on joint activities. The collaboration aims at capacity building in the areas of specialized electronics domains such as Embedded Systems, VLSI etc., through effective utilization of resources available with Government institutions of similar area of interest. This is the first step towards successful interaction amongst institutions of higher learning & research institutes across different ministries.

In a brief function held at DIAT Girinagar Campus on 21st April, Cmde (Retd.) A K Sinha, Registrar, DIAT & Dr M P Pillai Executive Director NIELIT Calicut executed the agreement in the presence of Hon Vice Chancellor of DIAT Dr Surendra Pal.



Prof.N Suresh Kumar, HoD Department of Electronics Engineering, Prof. S.K.Panigrahi Chairman PGC, J Jayaraj Senior Scientist NIELIT Calicut Deans (DIAT) were also present during the occasion.



As a first interaction a joint M Tech program in VLSI & Embedded systems is announced from this academic year. Selected students will undergo the program in both institution by spending one year each location.

Dr M P Pillai Executive Director NIELIT Calicut after signing agreement expressed that, this an unique initiative and NIELIT is proud in associating with DIAT in collaborative academic & research activities in high end electronics. This is a first step towards effective resource sharing and promote affordable VLSI design and tape-out in the areas of mutual interest. The facilities created at NIELIT Calicut under the support received from Ministry of Electronics & Information Technology, Government of India through Special Manpower Development Program –Chip to System Design (SMDP-C2SD) would be utilized for training the manpower.

Both institutions having expertise in advance electronics domains foresee that there is a vast scope for development of qualified manpower in the areas of VLSI, Embedded Systems and there is continuous progress in these domains. Through this initiative, both institution together facilitate the capabilities of young talent pool available in the country in general & research organisations in particular to make them ready for meeting the requirement of high-end electronic design areas.